Amendments to the Claims:

Listing of Claims:

Claim 1 (currently amended) A method for manufacturing a polysilicon thin film transistor liquid crystal display, the polysilicon thin film liquid crystal display comprising:

a panel;

a plurality of display cells, each display cell having at least a polysilicon thin film transistor;

a timing control circuit for generating a timing signal; and

a plurality of logic circuits for controlling operations of the display cells according to the timing signal;

the method comprising:

forming the plurality of display cells in the panel;

forming the plurality of logic circuits in the panel; and

determining a location in the panel for forming the timing control circuit so as to make differences among delay time intervals of the timing signals transmitted to [[the]] <u>different</u> logic circuits less than 1000 μ s, and forming the timing control circuit accordingly.

Claim 2 (currently amended) The method of claim 1 wherein the timing signal is transmitted to [[the]] <u>different</u> logic circuits by a plurality of transmitting lines, and differences between a product of an equivalent resistance value and an equivalent capacitance value of each transmitting line are less than 1000 μ s.

Claim 3 (original) The method of claim 1 wherein the polysilicon thin film transistor further comprises a plurality of scan lines and data lines connected to the display cells, and the plurality of logic circuits further comprise:

a scan line driving circuit connected to the plurality of scan lines;

a first data line driving circuit connected to the data lines of a first group; and

a second data line driving circuit connected to the data lines of a second group, the data lines of the first group and the data lines of the second group being arranged alternately.

Claim 4 (original) The method of claim 3 wherein the timing signal is respectively transmitted to the first data line driving circuit and to the second data line driving circuit by a first transmitting line and a second transmitting line, differences between a product of an equivalent resistance value and an equivalent capacitance value of the first transmitting line and a product of an equivalent resistance value and an equivalent capacitance value of the second transmitting line being less than 1000 μ s.

Claim 5 (original) The method of claim 1 wherein the polysilicon thin film liquid crystal display further comprises an interface circuit for receiving and transmitting an image signal such that the display cells operate according to the image signal.